# Optimal placement of PMU for complete observability of the interconnected power network considering zero-injection bus: A numerical approach

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## ABSTRACT

This paper presents an approach to place the phasor measurement unit (PMU) optimally, which minimizes the setup cost of PMU. This methodology attains complete state estimation of the interconnected power networks. An integer linear programming (ILP) method is explored for the optimal PMU placement problem. It is used to determine the optimal location and minimum number of PMUs necessary to make the interconnected power network completely observable. ILP may provide many solutions if acquainting buses to zero injection buses are unhandled. In the case of more than one solution, a bus observability redundancy index and total system observability redundancy index is proposed to find the most promising solutions set for redundancy measurement. The proposed algorithm is applied to benchmark the optimal PMU placement solutions for the IEEE 14-bus, IEEE 30-bus, New England 39-bus, IEEE 118-bus, and NRPG 246-bus test systems. The obtained results of the proposed approach are compared with the existing standard algorithm, and it is observed that the proposed approach achieves complete observability of the interconnected power network under base-load conditions.

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#### 1. INTRODUCTION

Interconnected power network measurement, synchronized on a universal basis, is shifting from the laboratory to the utility. PMU-an instrument that uses global positioning satellite (GPS) technology, provides novel opportunities for the interconnected power network monitoring, observability, control, and protection [1, 2]. In recent years, supervisory control and data acquisition (SCADA) systems are used for surveillance of the network conditions, but it gives unsynchronized measurements dominating the inconsistent estimation of the power network states [3]. Furthermore, a scan rate of data (2-4 samples per cycle) makes the SCADA system inefficient for measuring the dynamic/transient behavior occurring in the power network. These issues can be vanquished by the advent of PMUs, which mitigate this problem by using GPS technology and makes precise measurements of the network states [4]. The PMU with a faster scan rate (25 samples per second) makes itself acceptable for the observability of the power networks. Although the placement of PMU at each bus in the interconnected power network would provide all the states of the network system, it is injudicious as PMU and its communication facilities are expensive. Thus, an appropriate methodology is necessary for the site selection of PMUs. PMU may measure voltage

and current phasors and covers other characteristics such as preservative actions. The goal of the present paper is restricted to find the minimum number and the optimal location of PMU for complete observability of the interconnected power network states under intact conditions.

An interconnected power network is said to be fully observable only when all of its states are uniquely measured [5-7]. The vigorous research activities on the issues of finding the minimum number and optimal locations of PMU have already been published in the open literature. Phadke et al. do pioneering work in the field of PMU development and its application in the mid-80s [2, 8]. Some scientists, engineers, and researchers believe that the deployment of PMU at each bus will escort to a simplified linear state estimator. However, later this problem is resolved in [1], as each PMU can measure not only the bus voltage but also the branch current incident to the bus. Hence, proper site selection of PMU can make the power network completely observable. In [9 and 10], a novel binary search algorithm is used to find the minimum number and optimal locations of PMU for interconnected power network state estimations. In [11], the authors used a binary particle swarm optimization technique for finding the optimal locations of PMU. In [12], a novel intelligent search-based technique for the placement of PMUs in connected power networks while maintaining complete observability is proposed.

For the optimal placement of PMU, a genetic algorithm-based procedure is developed in [13]. In [14, 15], the authors proposed a topologically based three-stage optimal PMU placement (OPP) approach for the observability of interconnected power networks. In [16], the authors suggested a novel investment decision model for finding the optimal location of PMUs that gives assurance of the complete observability of the power grid. An ILP and multi-criteria decision-making based approach is proposed in [17] for placing the PMUs in multiple stages over a given period that guarantees fully interconnected power network observability even during a line outage or a PMU collapse. In [18, 19], authors used a multi-objective biogeography based optimization algorithm for site selection of PMU which makes the power network fully observable. In [20, 21], the authors used integer programming to find the minimum number and optimal locations of PMU for state estimation. In [22, 23], the problem related to OPP and conventional power flow measurements to assure observability during faulted conditions in power networks are considered. In that, at the beginning, the methodology is presented as a nonlinear integer programming problem and then changed into a similar ILP problem through Boolean suggestions. An integer programming based methodology is used by the authors in [24, 25] for the OPP problem in the interconnected power network. In [26], the authors explored the consequences of channel capacity of PMUs on their optimal locations to ensure that the interconnected power network is fully observable. In [27], the authors proposed a new methodology for the OPP problem in a connected power network that is suffering from random component outages. In [28], the authors proposed the sum of the variance of the robust estimators to determine the PMU placement problem. Also, the placements obtained are further illustrated based on the variance of the estimated states. Both the weighted least squares and robust estimators are taken into consideration. The OPP problem is evaluated as a binary semidefinite programming model with binary decision variables in [29]. Both single PMU and line outage is considered.

Two deterministic formulations are proposed in [30], which are mixed-integer linear programming and nonlinear programming, for solving the OPP problem to achieve complete power network observability. The authors have proposed a novel combinatorial formulation for monitoring the complete power grid in [31]. A multi-criteria decision support method, analytical hierarchy process, has been used to solve the OPP problem. The Pareto approach by nondominated sorting genetic algorithm II is proposed in [32] to minimize the PMU placement cost with the current channel selection and the state estimation error. In [33, 34], the two-phase branch-and-bound algorithm is proposed for unraveling the OPP problem. The main contribution of the presented work is to exclude radial buses (RBs) from the list of potential locations for employing a PMU because a PMU located at a radial bus can measure the voltage phasors at that bus and only one additional bus which is associated to it. PMU installed at a bus linked with the RB can measure the voltage phasor of the radial bus by using the measurement of the current phasor through the radial line. Therefore, a PMU is pre-assigned to each bus connected to a radial bus.

In this paper, we propose an integer linear programming algorithm for finding the minimum number and optimal locations of PMUs for the observability of the interconnected power network states. A different methodology that is numerical and uses integer programming is conferred. This method enables the unchallenging investigation of power network observability for mixed measurement sets. The developed criterion has removed any redundancies in PMU placement attained from the proposed algorithm. The proposed method is applied on IEEE 14-bus [35], IEEE 30-bus [35], New England (NE) 39-bus [36], IEEE 118-bus [35], and northern regional power grid (NRPG) 246-bus [37] test systems. The results obtained with the help of the proposed approach have been compared with the existing standard results published in the open literature to ensure the efficacy of the proposed method in OPP issues. The remaining paper is divided into the following segments: Section 2 explains the research methodology. In section 3, case studies and details of results are given, and in section 4, the conclusion of the paper is presented.

#### 2. RESEARCH METHOD

PMU placed at a specific bus in power network precisely measures the voltage phasor of the bus and the phasor currents for all branches incident to that bus [9]. Rely on the kind of PMUs used, the number of channels used for estimating voltage phasor and current phasor will differ. Therefore, the complete power network observability can be achieved by placing a PMU at a specific bus in the system. The main goal of the OPP problem is to furnish an observable network by using a minimum number and optimal locations of PMUs [38]. For the graphical representation of the OPP, IEEE 14-bus system is shown in figure 1 as an example [39]. In the interconnected power network, there is a bus known as a zero-injection bus (ZIB) with no generation and no loads connected to it. This ZIB minimizes the number of required PMUs by one because it can be merged with one of its adjacent buses. This means that the network topology is modified by using the merging process. It needs to redefine the network equations to reflect these changes [40].

In IEEE 14-bus system, three PMUs are located at  $B_2$ ,  $B_6$ , and  $B_9$ .  $B_7$  is only a zero injection bus (ZIB). The PMU at  $B_2$  not only measure the phase voltage of  $B_2$ , but also the phase current of  $L_1$ ,  $L_2$ ,  $L_3$  and  $L_4$ . The phase voltage at  $B_1$ ,  $B_3$ ,  $B_4$  and  $B_5$  can be acquired from the line currents by using Ohm's law. After finding phase voltages at  $B_1$ ,  $B_2$ ,  $B_3$ ,  $B_4$  and  $B_5$ , the phase current of  $L_4$ ,  $L_6$  and  $L_7$  are determined. Similarly, PMU at  $B_6$  measures phase voltage at  $B_6$  and the phase currents of  $L_8$ ,  $L_{14}$ ,  $L_{15}$  and  $L_{16}$  are also measured. Again by using Ohm's law, the phase voltages at  $B_5$ ,  $B_{11}$ ,  $B_{12}$ , and  $B_{13}$  can be measured and phase current of  $L_{19}$  can be determined. Further, PMU at  $B_9$  can measure the phase voltage at  $B_9$  and phase currents of  $L_{11}$ ,  $L_{12}$ ,  $L_{13}$  and  $L_{17}$  and by applying Ohm's law, the phase voltages at  $B_4$ ,  $B_7$ ,  $B_{10}$  and  $B_{14}$  can be evaluated. In addition, the phase current of  $L_9$  is also determined. As a result, phase voltages of  $B_{10}$ ,  $B_{11}$ ,  $B_{13}$  and  $B_{14}$  are known, phase currents of  $L_{18}$  and  $L_{20}$  are also estimated. By using phase currents of  $L_9$  and  $L_{12}$ , the phase current of  $L_{10}$  can also be evaluated by using Kirchhoff's current law (KCL). The phase voltage at a radial bus,  $B_8$ , can now be determined by utilizing the phase voltage at  $B_7$  and the phase current of  $L_{10}$ . Hence, the complete IEEE 14-bus system is observable by placing PMUs at  $B_2$ ,  $B_6$ , and  $B_9$  and considering  $B_7$  as a ZIB.

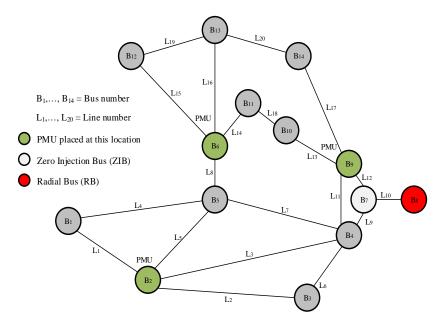


Figure 1. Optimal placement of PMU for IEEE 14-bus test system [39]

#### 2.1. ILP based PMU placement method

The integer programming (IP) is a numerical optimization programming for issues having integer variables, and it is the most common method for unraveling the OPP problem. IP is mentioned as integer linear programming (ILP) when the constraints and objective function are linear. In an ILP, when some variables are integers and other non-integers, then ILP is mentioned as mixed-ILP (MILP). In case

(6)

the variables are confined within [0, 1] then ILP can be treated as binary- ILP (BILP) technique. Therefore, the constraints played a significant role when using the ILP method to unravel the OPP issue.

## 2.1.1. OPP problem formulation

The objective of the OPP is to obtain the minimum number of PMUs needed and their locations for achieving full observability of the interconnected power network. Thus, the OPP is formulated as follows:

$$\min \sum_{k=1}^{n} c_k \cdot y_k$$

$$s.t. \ a(Y) \ge b$$

$$Y = [y_1 \ y_2 \ y_3 \ \dots \ y_n]^T$$
(2)

$$y_k \in \{0,1\}$$

where, *n* is the number of buses in interconnected power network for the deployment of PMUs,  $C_k$  is the cost of PMU set-up at  $k^{th}$  bus, *Y* is the binary decision variable vector having element  $y_k$  which decides achievability of PMU on  $k^{th}$  bus and whose entries are defined as in (3).  $\alpha(Y)$  are the observability constraints whose entries are non-zero if the bus voltage is noticeable w.r.t. the given sets of measurement and zero otherwise.

$$y_{k} = \begin{cases} 1, & \text{if PMU is set-up at } k^{th} & \text{bus} \\ 0, & & \text{otherwise} \end{cases}$$
(3)

The entries in a are as follows:

$$a_{k,m} = \begin{cases} 1, & \text{if bus } k \text{ is linked to bus } m \\ 1, & \text{if } k = m \\ 0, & \text{otherwise} \end{cases}$$
(4)

and

$$b = [1 \ 1 \ 1 \dots 1 \ 1]^T \tag{5}$$

The methodology for developing the constraint equation is explained for two attainable conditions (1) when there are no conventional measurements or ZIBs and (2) considering ZIBs [39]. The IEEE 14-bus test system is taken as an example to describe the above mentioned cases.

Case 1: A system with no conventional measurements.

In this case, ZIBs are ignored. For the sake of building the constraint set, the binary connectivity matrix a, as defined in (4), is constituted first.st. The a ( $n \times n$ ) matrix for the IEEE 14-bus test system is given in (6).

The constraints for this condition can be developed as (7).

 $\geq 1$  $f_1 = y_1 + y_2 + y_5$  $f_2 = y_1 + y_2 + y_3 + y_4 + y_5$ >1 $f_3 = y_2 + y_3 + y_4$ >1 $f_4 = y_2 + y_3 + y_4 + y_5 + y_7 + y_9 \ge 1$  $f_5 = y_1 + y_2 + y_4 + y_5 + y_6$  $\geq 1$  $f_6 = y_5 + y_6 + y_{11} + y_{12} + y_{13} \ge 1$  $f(Y) = a.Y = \begin{cases} J_6 = y_5 + y_6 + y_{11} + y_{12} + y_{13} \\ f_7 = y_4 + y_7 + y_8 + y_9 \\ f_8 = y_7 + y_8 \\ f_9 = y_4 + y_7 + y_9 + y_{10} + y_{14} \\ f_{10} = y_9 + y_{10} + y_{11} \\ f_{11} = y_6 + y_{10} + y_{11} \\ f_{12} = y_6 + y_{12} + y_{13} \\ f_{13} = y_6 + y_{12} + y_{13} + y_{14} \\ f_8 = y_8 + y_8 + y_8 + y_{14} + y_{14} \\ f_8 = y_8 + y_8 + y_8 + y_{14} + y_{14} \\ f_8 = y_8 + y_8 + y_8 + y_{14} + y_{14} \\ f_8 = y_8 + y_8 + y_8 + y_{16} + y_{16} \\ f_{13} = y_8 + y_{12} + y_{13} + y_{14} \\ f_{13} = y_8 + y_8 + y_8 + y_{16} + y_{16} \\ f_{13} = y_8 + y_{12} + y_{13} + y_{14} \\ f_{13} = y_8 + y_8 + y_8 + y_{16} + y_{16} \\ f_{13} = y_8 + y_8 + y_8 + y_{16} + y_{16} \\ f_{13} = y_8 + y_8 + y_{16} + y_{16} \\ f_{13} = y_8 + y_8 + y_8 + y_{16} + y_{16} \\ f_{13} = y_8 + y_8 + y_{16} + y_{16} \\ f_{13} = y_8 + y_{16} + y_{16} + y_{16} \\ f_{13} = y_8 + y_8 + y_{16} + y_{16} \\ f_{13} = y_8 + y_8 + y_{16} + y_{16} \\ f_{13} = y_8 + y_8 + y_{16} + y_{16} \\ f_{13} = y_8 + y_8 + y_{16} + y_{16} \\ f_{13} = y_8 + y_8 + y_{16} + y_{16} \\ f_{13} = y_8 + y_8 + y_{16} + y_{16} \\ f_{13} = y_8 + y_8 + y_{16} + y_{16} \\ f_{13} = y_8 + y_{16} + y_{16} + y_{16} \\ f_{13} = y_8 + y_{16} + y_{16} + y_{16} \\ f_{13} = y_8 + y_8 + y_{16} + y_{16} \\ f_{13} = y_8 + y_8 + y_{16} + y_{16} \\ f_{13} = y_8 + y_8 + y_8 + y_{16} \\ f_{13} = y_8 + y_8 + y_8 + y_{16} \\ f_{13} = y_8 + y_8 + y_8 + y_{16} \\ f_{13} = y_8 + y$  $\geq 1$  $f_{14} = y_9 + y_{13} + y_{14}$ > 1

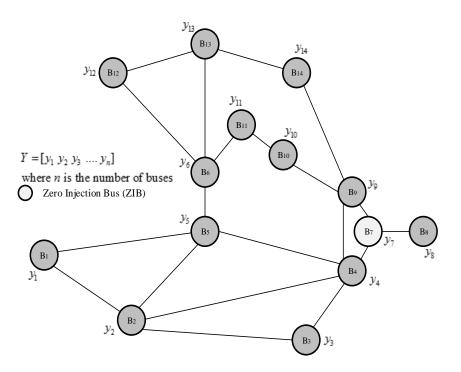
The operator '+' represents the logical 'OR' and the benefit of 1 in the right-hand side of the inequality assures that not less than one of the variables present in the sum will be non-zero; e.g. consider the constraint related with  $B_1$  and  $B_2$  as stated in (8).

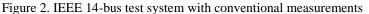
$$\begin{aligned} f_1 &= y_1 + y_2 + y_5 &\geq 1 \\ f_2 &= y_1 + y_2 + y_3 + y_4 + y_5 \geq 1 \end{aligned}$$
 (8)

The constraint  $f_1 \ge 1$  signifies that a PMU must be installed either at B<sub>1</sub>, B<sub>2</sub> or B<sub>5</sub> to observed B<sub>1</sub>. Likewise, the constrain  $f_2 \ge 1$  shows that a PMU must be installed at any one of the B<sub>1</sub>, B<sub>2</sub>, B<sub>3</sub>, B<sub>4</sub> or B<sub>5</sub> so that B<sub>2</sub> becomes observable.

Case 2: A system with zero injection (ZI) measurements.

In this case, we contemplate the exceedingly common position where zero injection measurements may exist but insufficient to make the interconnected power network completely observable. Again, the IEEE 14-bus test system, as shown in Figure 2 is considered, where  $B_7$  is a ZIB. It is obvious to identify that if the phase voltages at each three out of the four,  $B_4$ ,  $B_7$ ,  $B_8$  and  $B_9$  are acknowledged, then the fourth one can be determined by using KCL at  $B_7$  where the total sum of the injected current is known. Therefore, the constraints associated with the ZI buses are to adapt with the acquainting buses of theses buses and build a set of non-linear constraints and this is concluded as depicted in Figure 2.





Optimal placement of PMU for complete observability of the interconnected power network... (Rohit Babu)

(7)

In IEEE 14-bus test system, the constraints associated with ZIB  $B_7$  with its acquainting buses  $B_4$ ,  $B_8$ , and  $B_9$  are altered as given in (9);

$$f_{4} = y_{2} + y_{3} + y_{4} + y_{5} + y_{7} + y_{9} + f_{7} \cdot f_{8} \cdot f_{9} \ge 1$$

$$f_{8} = y_{7} + y_{8} + f_{4} \cdot f_{7} \cdot f_{9} \ge 1$$

$$f_{9} = y_{4} + y_{7} + y_{9} + y_{10} + y_{14} + f_{4} \cdot f_{7} \cdot f_{8} \ge 1$$
(9)

The operator '.' represents the logical 'AND' in (9). The explanations for  $f_i$  are clear by utilizing the following features of the logical AND (.) and OR (+) operators. A+B=B and A.B=A. Substituting for  $f_7$  from (7) in  $f_4$  of (9), yields:

$$f_{4} = y_{2} + y_{3} + y_{4} + y_{5} + y_{7} + y_{9} + f_{7} \cdot f_{8} \cdot f_{9}$$

$$= y_{2} + y_{3} + y_{4} + y_{5} + y_{7} + y_{9} + (y_{4} + y_{7} + y_{8} + y_{9}) \cdot f_{8} \cdot f_{9}$$

$$= y_{2} + y_{3} + y_{4} + y_{5} + y_{7} + y_{9} + y_{4} \cdot f_{8} \cdot f_{9} + y_{7} \cdot f_{8} \cdot f_{9} + y_{8} \cdot f_{8} \cdot f_{9} + y_{9} \cdot f_{8} \cdot f_{9}$$

$$= y_{2} + y_{3} + y_{4} + y_{5} + y_{7} + y_{9} + y_{8} \cdot f_{8} \cdot f_{9}$$

$$= y_{2} + y_{3} + y_{4} + y_{5} + y_{7} + y_{9} + y_{8} \cdot f_{8} \cdot f_{9}$$
(10)

In (10),  $y_4 \cdot f_8 \cdot f_9$  is removed as it is the subset of  $y_4$ . Similarly,  $y_7 \cdot f_8 \cdot f_9$  and  $y_9 \cdot f_8 \cdot f_9$  are also eliminated. It is observed that the explanation for  $f_7$  should also indulge an ancillary product term given by  $f_4 \cdot f_8 \cdot f_9$ , however this term will be dilapidated. In each simulated condition, this estimation is found ineffective in the optimization process. Further, substituting for  $f_8$  in (10), gives:

$$f_4 = y_2 + y_3 + y_4 + y_5 + y_7 + y_9 + y_8 \cdot f_8 \cdot f_9$$
  
=  $y_2 + y_3 + y_4 + y_5 + y_7 + y_9 + y_8 \cdot (y_7 + y_8) \cdot f_9$   
=  $y_2 + y_3 + y_4 + y_5 + y_7 + y_9 + y_8 \cdot f_9$  (11)

again, substituting for  $f_9$  in (11) yields:

$$f_4 = y_2 + y_3 + y_4 + y_5 + y_7 + y_9 + y_8 \cdot f_9$$
  
=  $y_2 + y_3 + y_4 + y_5 + y_7 + y_9 + y_8 \cdot (y_4 + y_7 + y_9 + y_{10} + y_{14})$   
=  $y_2 + y_3 + y_4 + y_5 + y_7 + y_9 + y_8 \cdot y_{10} + y_8 \cdot y_{14}$  (12)

similarly, implementing the same logic to other expressions, the constraint set can be written as (13).

$$f_{4} = y_{2} + y_{3} + y_{4} + y_{5} + y_{7} + y_{9} + y_{8} \cdot y_{10} + y_{8} \cdot y_{14} \ge 1$$

$$f_{8} = y_{4} + y_{7} + y_{8} + y_{9} \ge 1$$

$$f_{9} = y_{4} + y_{7} + y_{9} + y_{10} + y_{14} + y_{2} \cdot y_{8} + y_{3} \cdot y_{8} + y_{5} \cdot y_{8} \ge 1$$
(13)

It is observed that the constraints analogous to each bus remain unchanged, as shown in (7). But the constraint for B7 where the injection is measured is removed from the constraint set as the constraints related to ZIBs are incidentally considered by the product terms to add to the constraints related to the acquainting buses. The constraint for this condition is given as (14).

	$\int f_1 = y_1 + y_2 + y_5$	$\geq 1$	
	$f_2 = y_1 + y_2 + y_3 + y_4 + y_5$	$\geq 1$	
	$f_3 = y_2 + y_3 + y_4$	$\geq 1$	
	$f_{4_{new}} = y_2 + y_3 + y_4 + y_5 + y_7 + y_9$		
	$f_5 = y_1 + y_2 + y_4 + y_5 + y_6$	$\geq 1$	
	$f_6 = y_5 + y_6 + y_{11} + y_{12} + y_{13}$	$\geq 1$	
f(V) =	$\begin{cases} f_7 = y_4 + y_7 + y_8 + y_9 \\ f_{8_{new}} = y_4 + y_7 + y_8 + y_9 \end{cases}$	$\geq 1$	(14)
$f(\mathbf{r}) = \mathbf{r}$	$\int f_{8_new} = y_4 + y_7 + y_8 + y_9$	$\geq 1$	(14)
	$f_{9\_new} = y_4 + y_7 + y_9 + y_{10} + y_{14} + y_2$		
	$f_{10} = y_9 + y_{10} + y_{11}$	$\geq 1$	
	$f_{11} = y_6 + y_{10} + y_{11}$	$\geq 1$	
	$f_{12} = y_6 + y_{12} + y_{13}$	$\geq 1$	
	$f_{13} = y_6 + y_{12} + y_{13} + y_{14}$	$\geq 1$	
	$\int f_{14} = y_9 + y_{13} + y_{14}$	$\geq 1$	

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# 2.1.2. Algorithmic steps to obtain OPP problem

To find the solution for integer linear/non-linear programming problem, TOMLAB/MINLP with MATLAB software package is used [41]. The implementation of the proposed algorithm to obtain OPP for an interconnected power network consisting 'n' no. of buses using TOMLAB/MINLP is explained as follows [42]:

- Step 1. Obtain binary connectivity matrix  $a(n \times n)$  using (4).
- Step 2. Define  $b_l$  (lower bound on (y, a, y)) and  $b_u$  (upper bound on (y, a, y)).
- Step 3. Explain 'c', cost of the installed PMUs at  $k^{th}$  bus.
- Step 4. Expound ' $y_l$ ,  $y_u$ ', bound on variables.
- Step 5. Initialize 'y' vector  $(y_0)$ , if empty set as 0.
- Step 6. Define '*IntVars*', each variable should be integer. This field is explained individually depending on the length. Variable indices should be in between [1, ..., n].
- Step 7. Explain ' fIP', upper bound of f(y). Only consider solutions < fIP epsilon, default 1e20.
- Step 8. Interpret the priorities of the integer variables '*VarWeight*'. Can be any values, but lower values means higher priority.
- Step 9. Assign routine for defining IP problem ' $\Pr ob = mipAssign(step1 to step8)$ '.
- Step 10. Display PMU locations  $N_{PMU}$  ' using *tomRun*, the universal driver routine.

#### 2.1.3. Bus observability index (BOI)

After obtaining the minimum number and optimal locations of PMUs, an expression for BOI is given as [39]. We can acknowledge BOI as a performance symbol at all aspects of optimization.

$$BOI = a_{(n\times n)} \cdot N_{PMU}^{T}$$
<sup>(15)</sup>

#### 2.1.4. Total system observability redundancy index (TSORI)

The TSORI, which is the sum of bus observability for all buses, is given as follows [39]:

$$TSORI = \sum_{i=1}^{N_{PMU}} a_{(n \times n)} \cdot N_{PMU}^{T}$$
(16)

where,  $N_{PMU}$  is the total optimal number of PMUs obtained from step 10 of Section 3.2, a is the binary connectivity matrix which is obtained from (4). As shown in (15) and (16) gives the BOI and TSORI for all the possible results of optimal locations of PMUs.

# 3. RESULTS AND DISCUSSION

The proposed approach for finding the minimum number and optimal locations of PMU has been applied on standard IEEE 14-bus, IEEE 30-bus, New England 39-bus, IEEE 118-bus and NRPG 246-bus test systems. Figure 3, shows the single line diagram of the NRPG 246 bus test system. TOMLAB/MINLP with MATLAB software package is used to perform the integer linear programming problem, as described in subsection 2.1.2. The technical configuration of the computer is Intel core: I3-2330M (2.2 GHz), L3 Cache: 3MB, and System Memory: 3GB DDR3 which is used for the simulation purpose. Table 1 shows the number and locations of zero injection buses (ZIBs) and radial buses (RBs) for the standard test systems. The proposed approach eliminates the RBs from the possible locations of PMUs. As a result, buses next to these RBs with higher branch connectivity are preferred as the PMU placement sites [10, 39]. Table 2 shows the minimum number of PMUs necessary to make the interconnected power network completely observable under usual operating states with computational time. Tables 4 and 5 shows the redundancy measurement for the standard test system ignoring and considering ZIBs.

The bus numbers are designated as  $B_1$ ,  $B_2$ ,  $B_3$ ,...,  $B_n$  where n = 1,2,3,...,N. The entries in the tables symbolize the number of times a bus phase angle is measured by the optimal locations of PMUs. For example, in the case of IEEE 14-bus system, ignoring ZIBs,  $B_4$  is observed three times.  $B_5$ ,  $B_7$ ,  $B_9$  are observed two times and the rest of the buses are observed once. When considering ZIBs,  $B_4$  and  $B_5$  are observed two times and the remaining buses are observed once. Similarly, for the other test systems, redundancy measurement is shown.

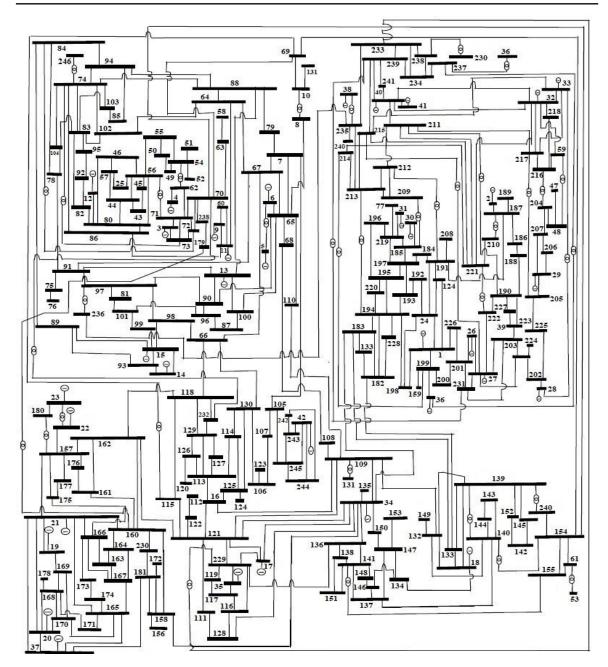


Figure. 3 Single line diagram of the NRPG 246 bus test system [37]

Test	Size (buses,	# of	ZIBs	# of	RBs
system	lines )	ZIBs		RBs	
14-bus	14, 20	01	B <sub>7</sub>	01	B <sub>8</sub>
30-bus	30, 41	06	$B_6, B_9, B_{22}, B_{25}, B_{27}, B_{28}$	03	$B_{11}, B_{13}, B_{26}$
39-bus	39, 46	11	$B_1, B_2, B_5, B_6, B_9, B_{11}, B_{13}, B_{14}, B_{17}, B_{19}, B_{22}$	09	$\begin{array}{l} B_{30}, B_{31}, B_{32}, B_{33}, B_{34}, B_{35}, B_{36}, B_{37}, \\ B_{38} \end{array}$
118-bus	118, 186	10	$B_5, B_9, B_{30}, B_{37}, B_{38}, B_{63}, B_{64}, B_{68}, B_{71}, B_{81}$	07	$B_{10}^{00}, B_{73}, B_{87}, B_{111}, B_{112}, B_{116}, B_{117}$
246-bus	246, 376	58	$\begin{array}{l} B_{54}, B_{56}, B_{59}, B_{61}, B_{62}, B_{63}, B_{69}, B_{70}, B_{71}, B_{72}, \\ B_{73}, B_{74}, B_{75}, B_{80}, B_{81}, B_{86}, B_{102}, B_{103}, B_{104}, \\ B_{107}, B_{122}, B_{126}, B_{129}, B_{131}, B_{147}, B_{154}, B_{155}, \\ B_{167}, B_{175}, B_{179}, B_{180}, B_{183}, B_{209}, B_{210}, B_{211}, \\ B_{212}, B_{213}, B_{214}, B_{215}, B_{216}, B_{217}, B_{221}, B_{222}, \\ B_{226}, B_{229}, B_{230}, B_{231}, B_{232}, B_{233}, B_{234}, B_{236}, \\ B_{237}, B_{238}, B_{239}, B_{240}, B_{241}, B_{243}, B_{244} \end{array}$	31	$\begin{array}{l} B_2, B_4, B_5, B_{12}, B_{30}, B_{31}, B_{38}, B_{41}, B_{47}, \\ B_{51}, B_{52}, B_{53}, B_{58}, B_{76}, B_{77}, B_{112}, B_{120}, \\ B_{123}, B_{124}, B_{135}, B_{149}, B_{153}, B_{156}, B_{159}, \\ B_{172}, B_{178}, B_{189}, B_{208}, B_{224}, B_{246} \end{array}$

Table 1. Location of zero injection buses (zibs) and radial buses (RBs)

Test system	Buses where PMU is needed for observability of the	radial buses
	Ignoring ZIBs	Considering ZIBs
14-bus	B <sub>7</sub>	
30-bus	$B_9, B_{12}, B_{25}$	B <sub>12</sub>
39-bus	$B_2, B_6, B_{10}, B_{19}, B_{20}, B_{22}, B_{23}, B_{25}, B_{29}$	$B_{10}, B_{20}, B_{23}, B_{25}, B_{29}$
118-bus	$B_9, B_{12}, B_{68}, B_{71}, B_{86}, B_{110}$	$B_{12}, B_{86}, B_{110}$
246-bus	$B_6, B_{34}, B_{40}, B_{48}, B_{54}, B_{61}, B_{62}, B_{63}, B_{74}, B_{75}, B_{80},$	$B_6, B_{34}, B_{40}, B_{48}, B_{106}, B_{125}, B_{132}, B_{158}, B_{168},$
	$B_{106}, B_{122}, B_{125}, B_{126}, B_{132}, B_{147}, B_{158}, B_{168}, B_{181},$	$B_{181}, B_{185}, B_{187}, B_{191}, B_{199}, B_{203}, B_{219}, B_{235}$
	$B_{185}, B_{187}, B_{191}, B_{199}, B_{203}, B_{219}, B_{235}$	

Table 2. Pre-allocation of PMUs

Table 3. Optimal locations of PMUs for standard test system with computational time

Test	Ignoring ZIBs Considerin		ring ZIBs	CPU (s)	Elapsed	
system	# of PMUs	Loc. of PMUs	# of PMUs	Loc. of PMUs		time (s)
14-bus	04	$B_2, B_6, B_7, B_9$	03	$B_2, B_6, B_9$	0.171	0.133
30-bus	10	$B_2, B_4, B_6, B_9, B_{10}, B_{12}, B_{15}, B_{19}, B_{25}, B_{27}$	07	$B_{3}, B_{5}, B_{10}, B_{12}, B_{19}, B_{23}, B_{27}$	0.140	0.154
39-bus	13	$B_2, B_6, B_9, B_{10}, B_{11}, B_{14}, B_{17}, B_{19}, B_{20}, B_{22}, B_{23}, B_{25}, B_{29}$	08	$B_3, B_8, B_{10}, B_{16}, B_{20}, B_{23}, B_{25}, B_{29}$	0.202	0.193
118-bus	32	$ \begin{array}{c} 222 & 237 & 237 & 257 \\ B_2, B_5, B_9, B_{12}, B_{15}, B_{17}, B_{21}, B_{23}, B_{28}, \\ B_{34}, B_{37}, B_{40}, B_{45}, B_{49}, B_{52}, B_{56}, B_{62}, B_{63}, \\ B_{68}, B_{71}, B_{75}, B_{77}, B_{80}, B_{85}, B_{90}, B_{94}, \\ B_{101}, B_{105}, B_{110}, B_{114} \end{array} $	28	$\begin{array}{l} B_2, B_{12}, B_{15}, B_{17}, B_{21}, B_{23}, B_{28}, B_{34}, \\ B_{37}, B_{40}, B_{45}, B_{49}, B_{52}, B_{62}, B_{63}, B_{68}, \\ B_{71}, B_{75}, B_{77}, B_{80}, B_{85}, B_{90}, B_{94}, \\ B_{101}, B_{105}, B_{110}, B_{114} \end{array}$	0.218	0.238
246-bus	70	$ \begin{array}{l} B_3, B_6, B_7, B_{10}, B_{11}, B_{18}, B_{21}, \\ B_{22}, B_{24}, B_{29}, B_{34}, B_{40}, B_{44}, B_{48}, B_{54}, B_{56}, \\ B_{57}, B_{61}, B_{62}, B_{65}, B_{70}, B_{74}, B_{75}, B_{78}, B_{80}, \\ B_{83}, B_{85}, B_{91}, B_{92}, B_{93}, B_{96}, B_{98}, B_{101}, \\ B_{106}, B_{109}, B_{113}, B_{117}, B_{121}, B_{125}, B_{128}, \\ B_{129}, B_{132}, B_{134}, B_{140}, B_{141}, B_{142}, B_{147}, \\ B_{157}, B_{158}, B_{160}, B_{163}, B_{168}, B_{173}, B_{181}, \\ B_{185}, B_{187}, B_{190}, B_{191}, B_{194}, B_{199}, B_{201}, \\ B_{203}, B_{203}, B_{215}, B_{216}, B_{219}, B_{229}, B_{234}, \\ B_{235}, B_{245} \end{array} $	62	$ \begin{array}{l} B_{3}, B_{6}, B_{7}, B_{10}, B_{11}, B_{18}, B_{21}, \\ B_{22}, B_{24}, B_{29}, B_{34}, B_{40}, B_{44}, B_{48}, B_{54}, \\ B_{56}, B_{57}, B_{65}, B_{78}, B_{80}, B_{83}, B_{85}, B_{91}, \\ B_{92}, B_{93}, B_{96}, B_{98}, B_{101}, B_{106}, B_{109}, \\ B_{113}, B_{117}, B_{121}, B_{125}, B_{128}, \\ B_{132}, B_{134}, B_{140}, B_{141}, B_{142}, \\ B_{157}, B_{158}, B_{160}, B_{163}, B_{168}, B_{173}, \\ B_{181}, B_{185}, B_{187}, B_{190}, B_{191}, B_{194}, \\ B_{199}, B_{201}, B_{202}, B_{203}, B_{219}, B_{229}, \\ B_{234}, B_{235}, B_{245} \end{array} $	0.374	0.468

Table 4. Redundancy	v measurement	for standard	test system	with ignoring (	ZIBs

Test	Bus observability index	Max.	TSORI
system		redundancy	
14-bus	1, 1, 1, 3, 2, 1, 2, 1, 2, 1, 1, 1, 1, 1	3	19
30-bus	1, 3, 1, 4, 1, 5, 1, 1, 3, 3, 1, 2, 1, 2, 2, 1, 1, 2, 1, 2, 1, 1, 1, 1, 2, 1, 2, 1, 1, 1	5	50
39-bus	1, 2, 1, 1, 1, 2, 1, 1, 1, 2, 3, 1, 2, 1, 1, 2, 1, 1, 1, 2, 1, 2, 2, 1, 2, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,	3	49
118-bus	1, 2, 2, 1, 1, 1, 1, 2, 1, 1, 2, 2, 1, 2, 2, 2, 2, 1, 2, 1, 1, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 2, 1, 1, 1, 3, 1, 2,	3	150
	2, 1, 2, 1, 1, 2, 1, 1, 1, 1, 1, 2, 1, 1, 2, 1, 1, 1, 1, 2, 1, 1, 1, 1, 1, 1, 1, 2, 1, 1, 2, 2, 1, 1, 1, 1, 2, 1, 3, 1,		
	1, 2, 2, 1, 1, 1, 1, 1, 1, 1, 2, 1, 1, 1, 1, 1, 2, 1, 1, 1, 1, 1, 1, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,		
246-bus	2, 1, 1, 1, 1, 2, 2, 2, 1, 1, 1, 1, 1, 1, 1, 3, 3, 2, 1, 2, 2, 2, 1, 2, 2, 1, 2, 1, 1, 1, 1, 1, 1, 2, 2, 1, 2, 1, 1, 1, 1, 1, 1, 1, 2, 2, 1, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,	4	360
	1, 1, 1, 1, 4, 2, 3, 1, 1, 1, 1, 1, 1, 1, 2, 2, 3, 2, 1, 2, 1, 1, 2, 1, 1, 3, 1, 3, 1, 2, 1, 3, 1, 2, 1, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,		
	1, 3, 1, 3, 1, 1, 1, 3, 1, 1, 1, 2, 3, 1, 1, 1, 2, 2, 2, 1, 1, 1, 2, 1, 2, 2, 2, 1, 1, 1, 3, 2, 2, 1, 2, 2, 1, 2, 1, 2, 1, 2, 1, 2, 1, 2, 1, 2, 2, 1, 1, 1, 2, 1, 1, 1, 2, 1, 1, 1, 2, 1, 1, 1, 2, 1, 1, 1, 2, 1, 1, 1, 2, 1, 1, 1, 1, 2, 1, 1, 1, 1, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,		
	1, 2, 1, 3, 1, 1, 1, 1, 2, 2, 1, 2, 2, 2, 3, 1, 3, 1, 3, 1, 1, 1, 2, 1, 1, 1, 1, 1, 1, 1, 2, 1, 1, 2, 1, 1, 3, 1, 1,		
	2, 4, 1, 3, 1, 3, 1, 2, 1, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,		
	1, 1, 2, 1, 2, 1, 2, 2, 2, 1, 1, 1, 1, 1, 1, 1, 1, 3, 1, 1, 1, 1, 1, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 2, 2, 2, 1, 2, 2, 2, 1, 2, 2, 2, 1, 2, 2, 2, 1, 2, 2, 2, 1, 2, 2, 2, 1, 2, 2, 2, 1, 2, 2, 2, 1, 2, 2, 2, 1, 2, 2, 2, 1, 2, 2, 2, 1, 2, 2, 2, 1, 2, 2, 2, 1, 2, 2, 2, 1, 2, 2, 2, 1, 2, 2, 2, 1, 2, 2, 2, 2, 2, 2, 2, 2, 2, 2, 2, 2, 2,		
	1, 1, 1, 2, 3, 1, 1, 1, 1, 1, 1, 1, 1		

Test	Bus observability index	Max.	TSORI
system		redundancy	
14-bus	1, 1, 1, 2, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1	2	16
30-bus	1, 1, 1, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,	2	31
39-bus	1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1	2	41
118-bus	1, 2, 2, 1, 1, 1, 1, 1, 1, 1, 1, 2, 1, 2, 2, 2, 2, 1, 2, 1, 1, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,	3	146
	1, 2, 1, 1, 2, 1, 1, 1, 1, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,		
	2, 1, 1, 1, 1, 1, 1, 2, 1, 1, 1, 1, 1, 1, 2, 1, 1, 1, 1, 1, 1, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,		
246-bus	2, 1, 1, 1, 1, 2, 2, 2, 1, 1, 1, 1, 1, 1, 1, 1, 3, 3, 2, 1, 2, 2, 2, 1, 2, 2, 1, 1, 1, 1, 1, 1, 1, 1, 2, 2, 1, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,	4	336
	1, 1, 1, 4, 2, 3, 1, 1, 1, 1, 1, 1, 1, 1, 2, 3, 2, 1, 1, 1, 1, 1, 1, 1, 3, 1, 3, 1, 1, 1, 2, 1, 1, 1, 1, 1, 2, 1, 3,		
	1, 3, 1, 1, 1, 2, 1, 1, 1, 2, 2, 1, 1, 1, 2, 2, 2, 1, 1, 1, 2, 1, 2, 1, 2, 1, 1, 1, 3, 2, 2, 1, 1, 2, 1, 2, 1, 1, 1, 2, 1, 2, 1, 1, 1, 2, 1, 2, 1, 1, 1, 2, 1, 2, 1, 1, 1, 2, 1, 2, 1, 1, 2, 1, 2, 1, 1, 2, 1, 2, 1, 1, 2, 1, 2, 1, 1, 2, 1, 2, 1, 1, 2, 1, 2, 1, 1, 2, 1, 2, 1, 1, 2, 1, 2, 1, 1, 2, 1, 2, 1, 1, 2, 1, 2, 1, 1, 2, 1, 2, 1, 1, 2, 1, 2, 1, 2, 1, 1, 2, 1, 2, 1, 2, 1, 1, 2, 1, 2, 1, 1, 2, 1, 2, 1, 2, 1, 1, 2, 1, 2, 1, 2, 1, 1, 2, 1, 2, 1, 2, 1, 1, 2		
	3, 1, 1, 1, 1, 1, 1, 1, 2, 2, 3, 1, 2, 1, 3, 1, 1, 1, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 2, 1, 1, 2, 4, 1, 3,		
	1, 3, 1, 2, 1, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,		
	1, 2, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 2, 1, 1, 1, 1, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 2, 2, 1, 1, 2, 1, 1, 1, 1, 2, 1, 1, 1, 1, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,		
	1, 1, 1, 1, 1		

In the present work, redundancy measurement is also more systematically assigned. For example, in the case of IEEE 14-bus the maximum redundancy for a bus is 3 (in case of ignoring ZIBs) and 2 (in case of considering ZIBs). The optimal number of PMU obtained using the proposed approach has been compared with the existing standard results found in the literature are shown in Table 6. It can be observed that the optimal number of PMUs necessary to make the interconnected power network (IEEE 14.30,118, New England 39, and NRPG 246-bus system) fully observable are somewhat the same as the results published in the open literature.

Table 6. Comparis	son of results wit	h the existing	standard approac	h under usual	operating states
rubie o. company	son or results wit	in the existing	stunduru upproue	ii unuor usuu	operating states

Test system	14-bus	30-bus	39-bus	118-bus	246-bus
S. Chakrabarti and E. Kyriakides [10]	4	10	13		
T. Venkatesh and T. Jain [12]	4	10			83
Roy et al. [15]	4	10	13	32	
S. Mousavian and M. J. Feizollahi [16]	4	10	13	32	
Sodhi et al. [17]	7		26		135
B. Xu and A. Abur [21]	4	10		32	
B. Gou [24]	4				
Proposed approach	4	10	13	32	70

Note: '--' entry means not reported

## 4. CONCLUSION

This paper consummated two goals. Firstly, an ILP based approach for finding optimal locations of PMU is explored and secondly, the explored approach is executed and tested on several standard test systems. The first step in placing the PMUs is the identification of a candidate location. In a power system, there may be certain buses that are strategically important, so that a PMU must be located at each of those buses. The remaining buses are made observable by installing a minimum number of additional PMUs. The radial buses are excluded from the list of potential locations for placing a PMU. As PMU placed at a radial bus can measure the voltage phasors at that bus and only one additional bus that is associated with it. Further, PMU placed at the bus connected to the radial bus can measure the voltage phasor of the radial bus by using the measurement of the current phasor through the radial line. Therefore, a PMU is pre-assigned to each bus connected to a radial bus. Pre-assigning PMUs to certain buses in this manner, the system observability is satisfied since no violation constraint exists. Thus, the number of PMUs are the same for each power system since the system observability is satisfied with the improvement that no radial buses are included in the optimal solutions. The offering of the paper remains in benchmarking the universal OPP solutions for a number of extensively used systems and determine the optimal locations of PMUs for the system. The obtained results indicate the efficacy of the proposed approach of OPP for interconnected power network observability. After comparing with the standard approaches in the earlier published literature, the simulation results show that the proposed approach determines the minimum number of PMUs, unlike earlier approaches which may find either the same or even higher number of PMUs.

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